

11046 U.S. PTO  
10/0065125  
09/19/02

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

| APPL NUM | FILING DATE | CLASS | SUBCLASS | GAU  | EXAMINER |
|----------|-------------|-------|----------|------|----------|
| 10065125 | 09/19/2002  | 327   | 147      | 2816 | 444      |

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351 251

\*\*CONTINUING DATA VERIFIED: 444

\*\* FOREIGN APPLICATIONS VERIFIED: 144

|   |   |  |                                    |
|---|---|--|------------------------------------|
| PG-PUB  | DO NOT PUBLISH <input type="checkbox"/> | RESCIND <input type="checkbox"/>   |                                    |
| Foreign priority claimed<br>35 USC 119 conditions met<br>Verified and Acknowledged Examiners's initials |   | <input type="checkbox"/> yes <input checked="" type="checkbox"/> no<br><input type="checkbox"/> yes <input checked="" type="checkbox"/> no | ATTORNEY DOCKET NO<br>2001P18279US |
| TITLE : Ferroelectric memory architecture   |   |  |                                    |

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev 12-94)

|  |           |                    |                |                     |
|--|-----------|--------------------|----------------|---------------------|
| NOTICE OF ALLOWANCE MAILED   |           | Assistant Examiner | CLAIMS ALLOWED |                     |
|  |           |                    | Total Claims   | Print Claim for O.G |
| ISSUE FEE  |           | DRAWING            |                |                     |
| Amount Due   | Date Paid | Shows Draw.        | Figo.Draw.     | Print Fig.          |
| TERMINAL   |           | Primary Examiner   |                |                     |
| DISCLAIMER   |           | PREPARED FOR ISSUE |                |                     |
| Application Examiner   |           |                    |                |                     |
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